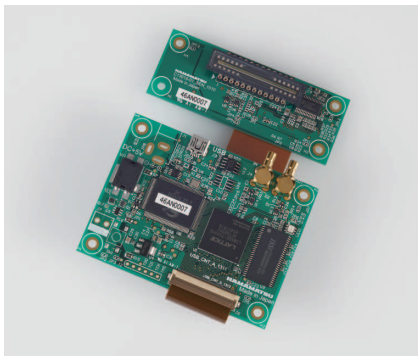


Driver circuit for CMOS linear image sensor



C13015-01

For CMOS linear image sensor S11639-01, etc.

The C13015-01 is a driver circuit developed for Hamamatsu CMOS linear image sensor S11639-01, etc. By connecting the C13015-01 to a PC through the USB 2.0 interface, you can control the C13015-01 from the PC and acquire 16-bit digital output numeric data converted from the sensor's analog video signal. The C13015-01 consists of a sensor circuit board that drives the sensor and an interface circuit board that drives the sensor circuit board and performs data communication with the PC. The two circuit boards are connected via a flexible cable. The sensor circuit board is compact, making it easy to be installed in optical systems. The interface circuit board has an external trigger I/O connectors that can be used to synchronize with external devices. This product comes with application software (DcIc-USB) that runs on Microsoft® Windows® 7 (32-bit, 64-bit)/10 (32-bit, 64-bit). It can be used to easily control the C13015-01 from the PC. The product also includes a DLL that the user can use to create original C13015-01 control programs.

Features

- Built-in 16-bit A/D converter
- Compact sensor circuit board: Easy to install in optical systems
- Interface: USB 2.0
- External synchronization capable
- Single power supply: USB bus powered (+5 VDC)

Application

- Spectrometers
- CMOS linear image sensor S11639-01, etc. control and data acquisition

Note) Microsoft and Windows are either registered trademarks or trademarks of Microsoft Corporation in the United States and/or other countries.

The C13015-01 is compatible with the following CMOS linear image sensor. Note that the C13015-01 does not include a sensor.

Type no.	Number of pixels	Number of effective pixels	Pixel size (μm)	Image size [mm (H) × mm (V)]
S11639-01	2048 × 1	2048 × 1	14 × 200	28.672 × 0.200
S11639-11	2048 × 1	2004 × 1	14 × 200	28.056 × 0.200
S12706	4096 × 1	4096 × 1	7 × 7	28.672 × 0.007
S13496	4096 × 1	4096 × 1	7 × 200	28.672 × 0.200
S13496-11	4096 × 1	4008 × 1	7 × 200	28.056 × 0.200

Structure

Parameter	Specification	Unit
Output type	Digital	-
A/D resolution	16	bit
Interface	USB 2.0	-

■ Absolute maximum ratings

Parameter	Symbol	Condition	Value	Unit
Supply voltage	Vdd	Ta=25 °C	0 to +6.0	V
Input signal voltage*1	Vi	Ta=25 °C	0 to +Vdd	V
Operating temperature	Topr	No dew condensation*2	0 to +50	°C
Storage temperature	Tstg	No dew condensation*2	-20 to +70	°C

*1: Trigger input

*2: When there is a temperature difference between a product and the surrounding area in high humidity environment, dew condensation may occur on the product surface. Dew condensation on the product may cause deterioration in characteristics and reliability.

Note: Exceeding the absolute maximum ratings even momentarily may cause a drop in product quality. Always be sure to use the product within the absolute maximum ratings.

■ Electrical characteristics (Ta=25 °C)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Readout frequency	fop		-	10	-	MHz
Line rate*3	-		-	-	4	kHz
Conversion gain	Gc	Gain=1	-	28	-	μV/ADU
Trigger output voltage	High level	Vdd=+5 V	3.8	-	Vdd	V
	Low level		-	-	0.6	V
Trigger input voltage	High level	Vdd=+5 V	+3.5	-	Vdd	V
	Low level		-	-	1.5	V
Current consumption	Ic		-	300	500	mA
High start pulse period*4 *6	thp(ST)		10	-	tpi(ST) - 200	clock*5
Start pulse period*4 *7	tpi(ST)		2500	-	4294967295	clock*5

*3: Theoretical line rate value determined by the internal operation timing of the driver circuit. This is different from the line rate defined in the sensor specifications. This is also different from the rate (system rate) in a series of processes that acquire data into the PC via the USB 2.0 port.

*4: thp(ST) < tpi(ST)

*5: 1 clock=1/fop

*6: A maximum value exists when the synchronization mode is internal mode, external edge mode, or external gate mode.

*7: A maximum value exists when the synchronization mode is internal mode or external gate mode.

■ Electrical and optical characteristics (Ta=25 °C)

■ When mounted with S11639-01 or S11639-11

Parameter	Symbol	Min.	Typ.	Max.	Unit
Readout noise	Nread	-	14	-	ADU rms
Saturation output	Dsat	-	56000	65535	ADU
Dynamic range*8	DR	-	4000	-	-

■ When mounted with S12706

Parameter	Symbol	Min.	Typ.	Max.	Unit
Readout noise	Nread	-	33	-	ADU rms
Saturation output	Dsat	-	55700	-	ADU
Dynamic range*8	DR	-	1700	-	-

■ When mounted with S13496 or S13496-11

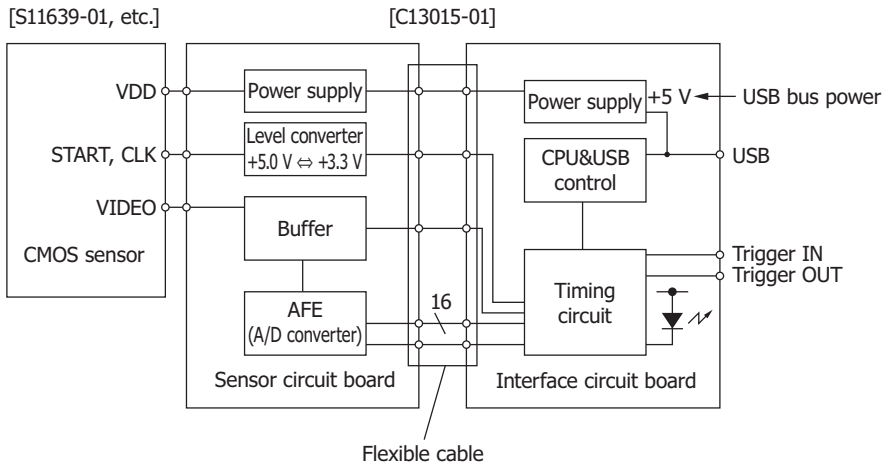
Parameter	Symbol	Min.	Typ.	Max.	Unit
Readout noise	Nread	-	16	-	ADU rms
Saturation output	Dsat	-	58300	-	ADU
Dynamic range*8	DR	-	3600	-	-

*8: DR=Dsat/Nread

Functions

Function	Description
Trigger mode change	The following trigger modes are available. For the detailed timing of each mode, see "Trigger mode" in "Timing chart" (P.4). <ul style="list-style-type: none"> · Internal mode · External edge mode · External level mode · External gate mode
Gain adjustment	The gain can be set to a value between 1 and 6 with a 6-bit number ranging from 0 to 63. The preset gain is calculated by the following equation. The default gain is 1. $\text{Gain} = \frac{6}{1 + 5 \left(\frac{63 - G}{63} \right)}$ G: 6-bit value
Offset adjustment	The offset can be set in the range of -255 to +255. The offset increment per step is approximately 1.2 mV. The offset is set by writing a 9-bit value to the C13015-01 internal register. If the most significant bit (MSB) of the 9-bit value is zero, the offset is positive. Otherwise, the offset is negative. The bit values other than the MSB are the magnitude of the offset. Note that because the offset circuit is arranged before the amplifier stage, the actual offset is equal to the offset value set above × gain.
Integration time change	The sensor integration time is changed by changing the high period of the ST pulse.
Start pulse period change	A maximum value exists for internal mode or external gate mode.

Block diagram

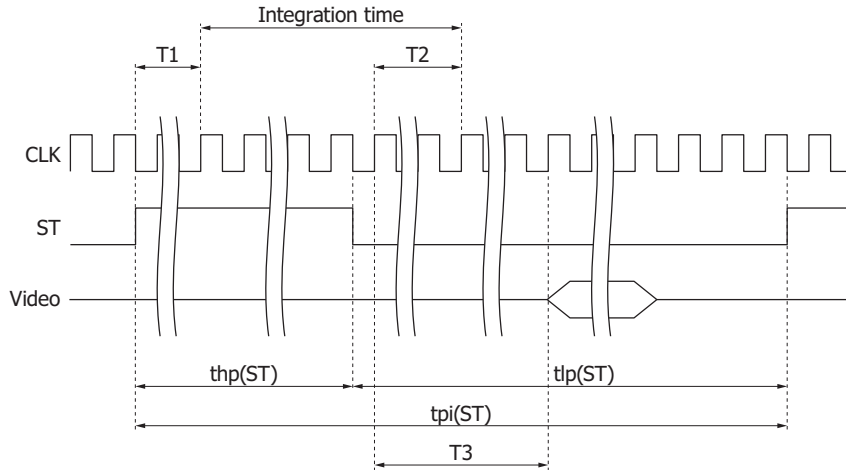


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Timing chart

Sensor drive timing

The two parameters that can be changed in the sensor drive timing are the ST pulse width $thp(ST)$ and the line period $tpi(ST)$. For the $thp(ST)$ and $tpi(ST)$ setting ranges in internal mode, external edge mode, and external gate mode (explained later), see "Electrical characteristics" (P. 2). The timings of T1, T2, and T3 when the S11639-01 is connected are as follows.



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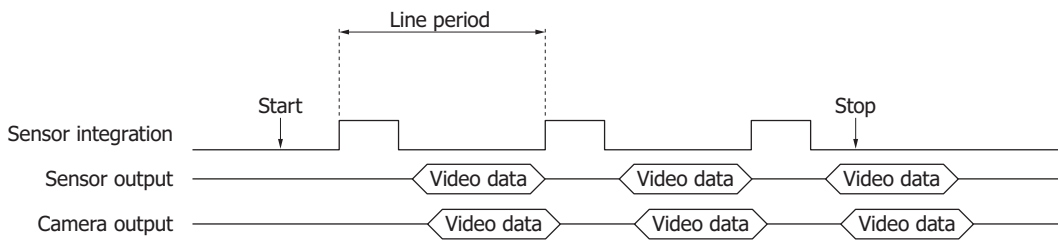
- T1=4CLK
- T2=52CLK
- T3=87CLK
- (1CLK=0.1 μ s)
- Integration time= $thp(ST) + 48CLK$

The following condition must be met.
 $tpi(ST) > thp(ST)$

Trigger mode

■ Internal mode

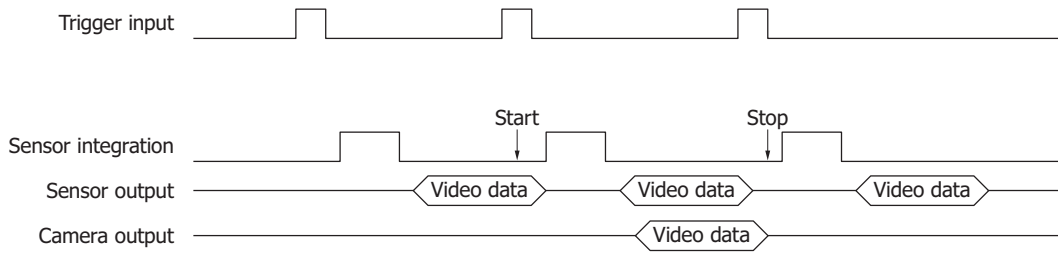
When a Start command is received from the PC, the C13015-01 starts sensor integration and outputs acquired image data.



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■ External edge mode

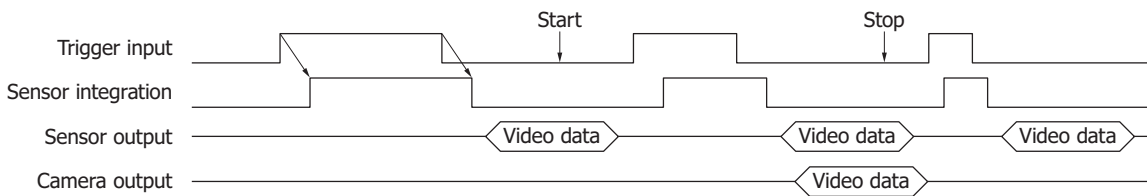
The sensor integration is synchronized to the edges of an external input trigger signal. When the circuit receives an external trigger after having received a Start command, the circuit sends the image data output from the sensor to the PC.



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■ External level mode

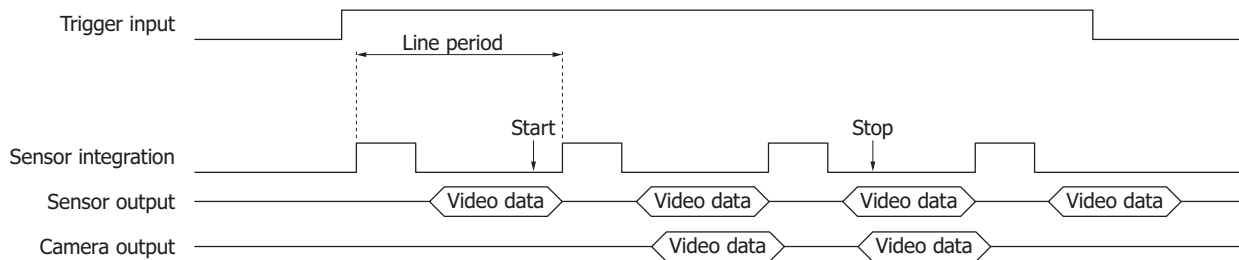
Integration time and line transmission period are controlled using an external trigger signal.



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■ External gate mode

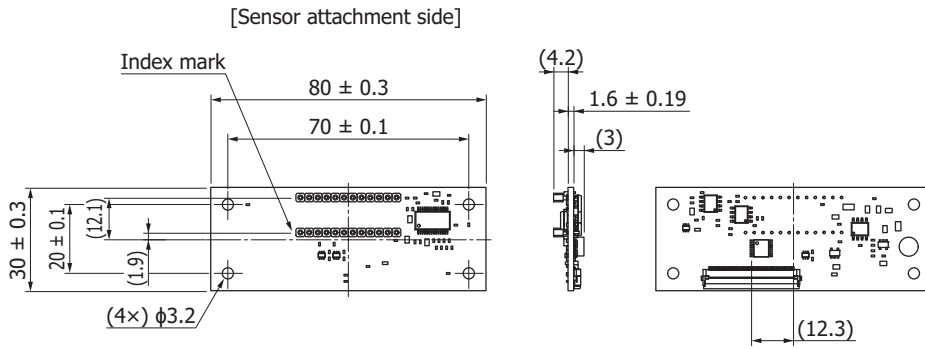
Integration is performed only while the external trigger is valid.



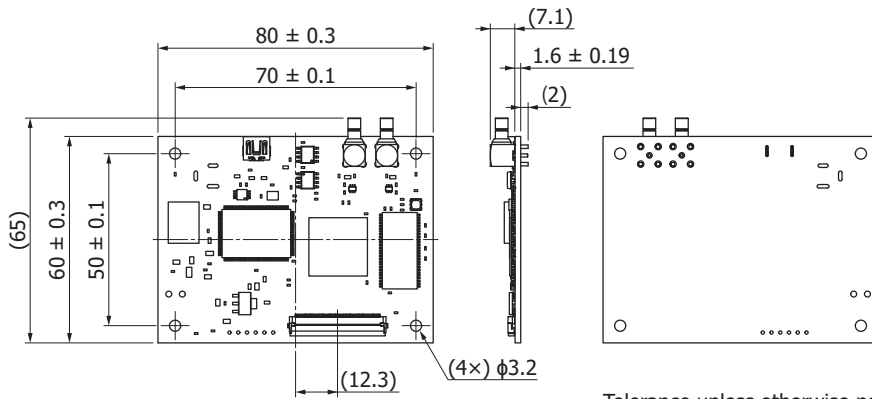
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Dimensional outline (unit: mm)

■ Sensor circuit board



■ Interface circuit board

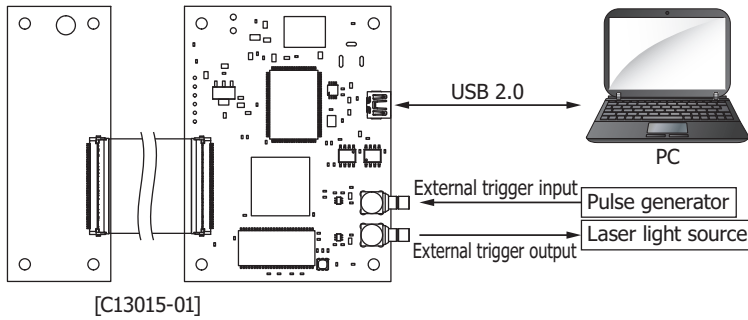


Tolerance unless otherwise noted
complies with JIS B 0408-B.

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Weight: approx. 40 g (including the flexible cable but not the sensor)

Connection example



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Accessories

- CD-ROM (includes the instruction manual, application software, and DLL file)
- USB (Mini B) cable
- Flexible cable for connecting sensor circuit board and interface circuit board

Related information

www.hamamatsu.com/sp/ssd/doc_en.html

- Precautions
 - Disclaimer
 - Image sensor

Information described in this material is current as of November 2019.

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